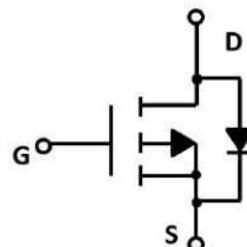
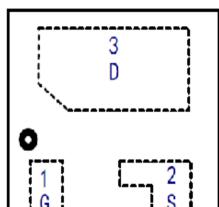


TM09P02AF3
P-Channel Enhancement Mosfet

General Description <ul style="list-style-type: none"> • Low $R_{DS(ON)}$ • RoHS and Halogen-Free Compliant Applications <ul style="list-style-type: none"> • Load switch • PWM 	General Features <p>$V_{DS} = -18\text{ V}$ $I_D = -9.0\text{ A}$</p> <p>$R_{DS(ON)} = 16\text{ m}\Omega(\text{typ.}) @ V_{GS} = -4.5\text{ V}$</p> <p>100% UIS Tested 100% R_g Tested</p>
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AF3:DFN1.5x1.5-3L

Marking: 09P02
Absolute Maximum Ratings (T_A = 25°C Unless Otherwise Noted)

Symbol	Parameter	Max.	Units
V _{DSS}	Drain-Source Voltage	-18	V
V _{GSS}	Gate-Source Voltage	± 12	V
I _D	Continuous Drain Current	T _A = 25°C	A
		T _A = 100°C	A
I _{DM}	Pulsed Drain Current ^{note1}	-33	A
P _D	Power Dissipation	T _A = 25°C	W
R _{θJA}	Thermal Resistance, Junction to Case	70	°C/W
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D= -250\mu\text{A}$		-18	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}= -15\text{V}$, $V_{GS}=0\text{V}$,	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0\text{V}$, $V_{GS}= \pm 12\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D= -250\mu\text{A}$	-0.4	-0.7	-1.0	V
$R_{DS(\text{on})}$ note2	Static Drain-Source on-Resistance	$V_{GS}= -4.5\text{V}$, $I_D= -7\text{A}$	-	16	20	$\text{m}\Omega$
		$V_{GS}= -2.5\text{V}$, $I_D= -5\text{A}$	-	21	25	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}= -10\text{V}$, $V_{GS}=0\text{V}$, $f=1.0\text{MHz}$	-	1800	-	pF
C_{oss}	Output Capacitance		-	242	-	pF
C_{rss}	Reverse Transfer Capacitance		-	231	-	pF
Q_g	Total Gate Charge	$V_{DS}= -10\text{V}$, $I_D= -3\text{A}$, $V_{GS}= -4.5\text{V}$	-	15.3	-	nC
Q_{gs}	Gate-Source Charge		-	2.2	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	4.4	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}= -10\text{V}$, $I_D= -7\text{A}$, $V_{GS}= -4.5\text{V}$, $R_{GEN}=2.5\Omega$	-	10	-	ns
t_r	Turn-on Rise Time		-	31	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	28	-	ns
t_f	Turn-off Fall Time		-	8	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current		-	-	-9	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-33	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s= -7\text{A}$	-	-0.8	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

Typical Performance Characteristics

Figure 1: Output Characteristics

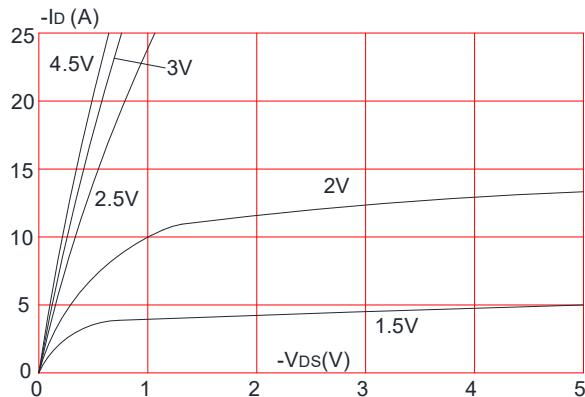


Figure 3: On-resistance vs. Drain Current

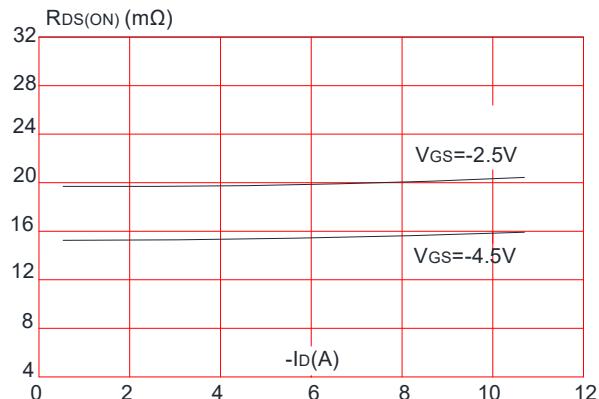


Figure 5: Gate Charge Characteristics

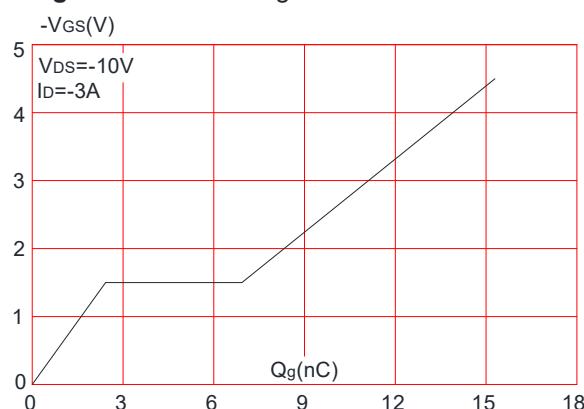


Figure 2: Typical Transfer Characteristics

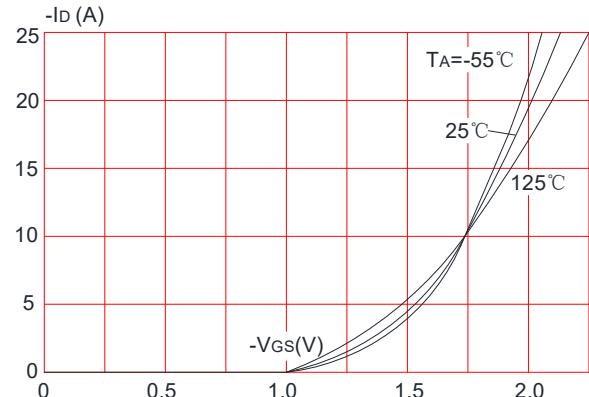


Figure 4: Body Diode Characteristics

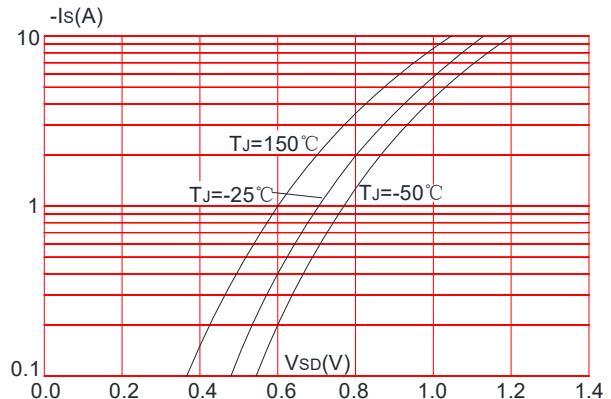


Figure 6: Capacitance Characteristics

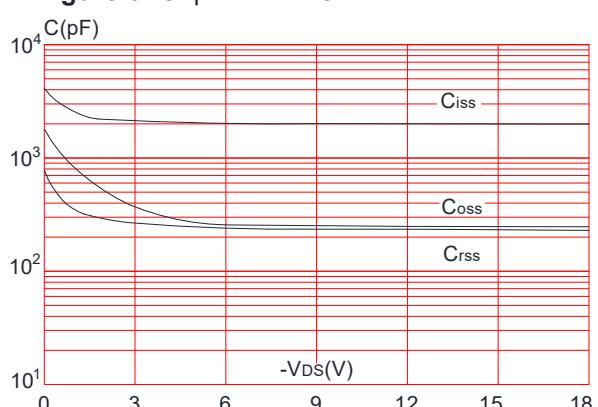


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

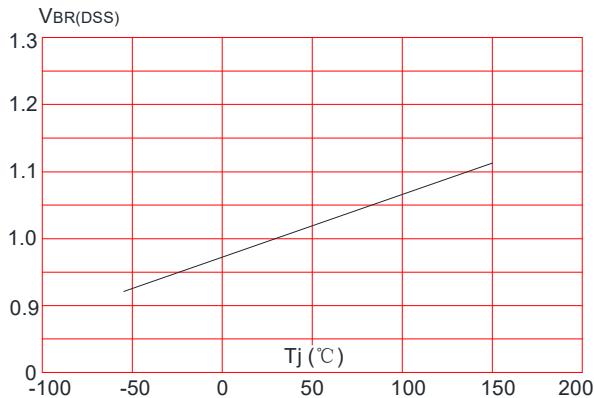


Figure 8: Normalized on Resistance vs. Junction Temperature

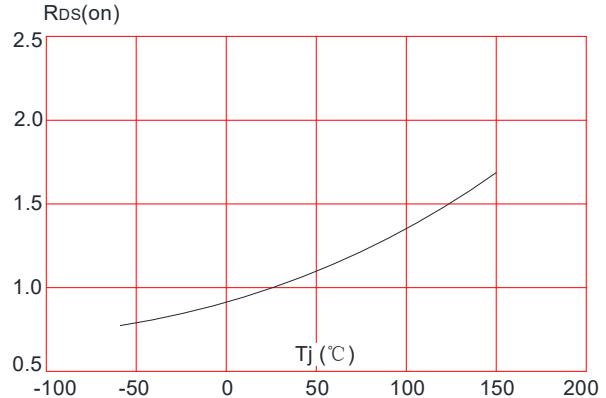


Figure 9: Maximum Safe Operating Area

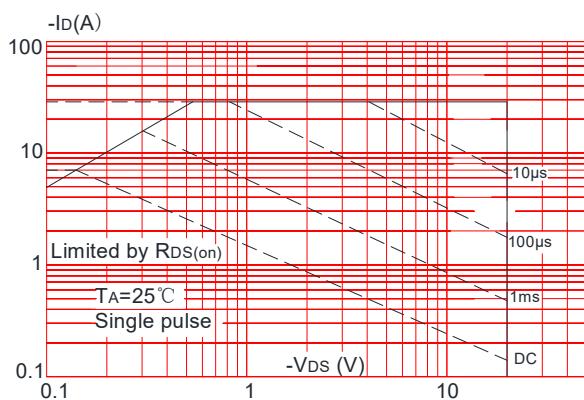


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

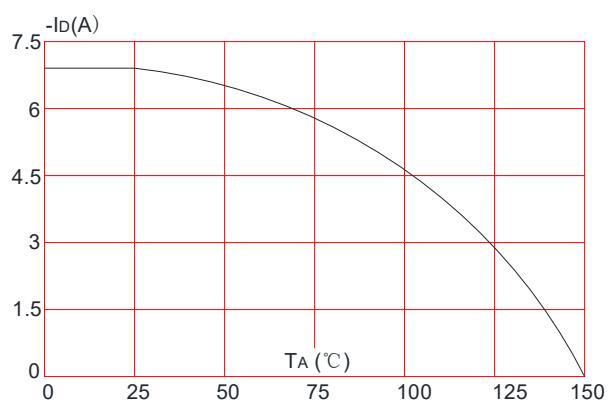
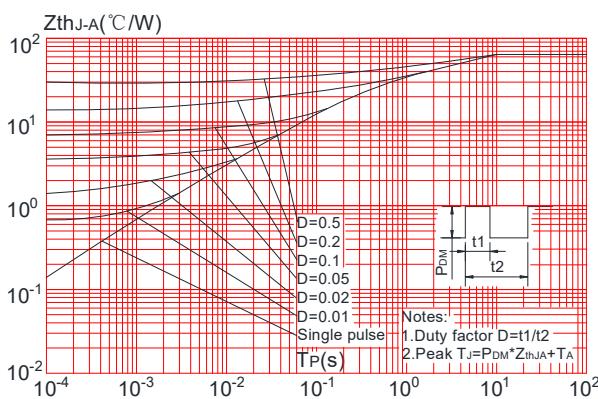
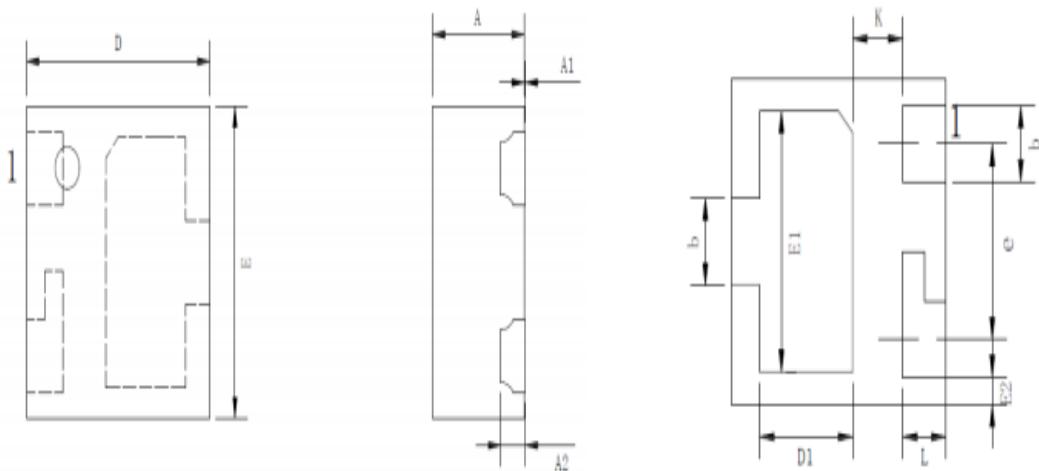


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



Package Information:DFN1.5x1.5-3L



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	—	—	0.80
A1	0.00	—	0.05
A2	0.203 TIY		
b	0.30	0.35	0.40
D	1.45	1.50	1.55
D1	0.60	0.65	0.70
E	1.45	1.50	1.55
E1	1.15	1.20	1.25
E2	0.125 TIY		
e	0.90 BSC		
K	0.35 BSC		
L	0.25	0.30	0.35